# M.Sc IT sem-1v Artificial Intelligence

### (REVISED COURSE) QP Code: 15723

(2½ hours)

Total Marks: 60

N. B.:	(1) All questions are compulsory.	
	(2) Make suitable assumptions wherever necessary and state the assumptions made.	
	(3) Answers to the same question must be written together.	
	(4) Numbers to the right indicate marks.	(
	(5) Draw neat labeled diagrams wherever necessary.	1
	(6) Use of Non-programmable calculators is allowed.	()
	0	
1.	Attempt any two of the following:	12
a.	What is Artificial Intelligence? Explain the characteristics of Artificial Intelligence	
b.	Give the outline of A* algorithm	
c.	What is temporal logic? Discuss the types of temporal logic.	
ď.	Explain the syntax and semantics of First Order Logic.	
u.	Explain are of the control of the co	
2.	Explain the syntax and semantics of First Order Logic.  Attempt <u>any two</u> of the following:	12
a.	Give the outline MinMax algorithm.	
a. b.	What is easy based reasoning? Explain the components of case based reasoning.	1
	Write the script for student starting from hostel to main road, then to classroom and	
c.	finally back to hostel.	
4	Explain in brief the knowledge representation using semantic nets.	
d.	Exhiam in order me who weeds representation	
2	Attempt any two of the following:	12
3.	Explain Dempster-Shafer theory of evidence with an example.	100
a.	Explain the theory of explanation based learning.	
ъ.	What is confidence factor? What are the different strategies for implementing	
c.	confidence factor?	
*	Explain any two knowledge acquisition tools.	
d.	Explain any two knowledge acquisition records	
	t two of the following: C-	12
4.	Attempt <u>any two</u> of the following: What is planning? Explain different types of planning.	
a.	What is planning? Explain difference types of planning.	
b.	Write a note on backtracking.	
C.	Explain the constraint satisfaction problem.	
d.	Give the outline of Generate and Test planning.	
	C (6 ( 1)	12
5.	Attempt any two of the following:	
a	Explain syntactically the List and Structures.	
b.	Discuss the structure of an expert system.	
C.	Give the comparative view of different expert system shell.	
d.	Discuss any two web based expert systems.	5

# M.sc comp.sci Image processing

QP Code: 15717

(2½ Hours)

[Total Marks: 60

N. B.:	(2) Figures to the right indicate full marks. (3) Assume additional data if necessary but state the same clearly.	
	(4) Symbols have their usual meanings and tables have their usual stan unless stated otherwise.	dard design
	(5) Use of calculators and statistical tables are allowed.	
Q.1	Attempt any two of the following	(12)
a)	Justify the following statement 'Brightness discrimination is poor at low levels of illumination'.	6
b)	Write and explain the expression for DFT applied to 2-D signal.	6 9
c)	What is imaging in the ultraviolet band. Explain.	6
ď)	Define and explain the following: i) Types of connectivity ii) FFT	6 /
Q.2	Attempt any two of the following	6 11
a)	State and explain the procedure to convert a non uniform histogram to uniform histogram.	6
<b>b</b> )	With an example explain the properties of first and second order derivative.	6
c)	Discuss the low pass frequency domain filters in image enhancement.	6
d)	Write the advantage of piecewise linear function. Hence, explain any	6
,	one method in this.	
Q.3	Attempt any two of the following	(12)
a)	State and explain any two application of grey scale morphology.	6
b)	Explain the procedure to construct the image pyramids.	6
c)	How region filling algorithm works. Explain with an example.	6
d)	How HIT-or-MISS transformation is used for finding the local	6
	patterns in image.Explain.	
Q.4	Attempt any two of the following	(12)
Q.1	Attompt <u>any cite</u> of the city	
a)	How does Huffman coding achieve compression. Explain.	6
b)	Describe briefly LoG operator.	6 .
c)	Discuss the thinning algorithm for finding skeleton of region.	6
ď)	Define psychovisual redundancy. Explain an approach to	6
	remove/reduce (t.)	
	$\mathcal{O}$	
Q.5	Attempt any two of the following	(12)
a)	What are spatial and grey level resolution. Explain.	6
a) b)	What is relation between illumination-reflectance model and	6
	image.Explain.	
c)	What do you mean by wavelet function. Explain.	6
ď)	Explain the following: i) Global thresholding ii) Chain number.	6
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C/O	***	
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MG-Con. 6850-15.

QP Code: 16068

(2½ hours)

Total Marks: 60

NT D	(1) All questions are compulsory.	
IV. D.	(2) Make <u>suitable assumptions</u> wherever necessary and <u>state the assumptions</u> made.	
	(3) Answers to the <u>same question</u> must be <u>written together</u> .	
	(4) Numbers to the <u>right</u> indicate <u>marks</u> .	
	(5) Draw <u>neat labeled diagrams</u> wherever <u>necessary</u> .	_
	(5) Draw near labeled diagrams wherever in ellowed	. \
	(6) Use of Non-programmable calculators is allowed.	1
	P.	12
1.	Attempt <u>any two</u> of the following: What are RISC and CISC processors? How can we distinguish between 8-bit and 189	12
a.	What are RISC and CISC processors? How can we distinguish between 8-bit and 1139	
b.	Discuss the criteria for selecting a microcontroller device.	
C.	With the help of a block diagram explain the Amtel 89C51 microcontroller.	
d.	With the help of the circuit diagram, explain the test circuit for 89C2051	
	$\sim$	
2.	Attempt any two of the following:	12
a.	Give an overview and features of peripheral interface controllers.	
b.	With the help of a block diagram the PIC 16C61.	
c.	Explain status registers and power control registers in PIC 165 XX.	
d.	Explain capture, compare and PWM modules in PIC16F877.	
u,	Explain supraise, some	
2	Attempt any two of the following:	12
3.	How does ARM instruction set differ from pure RISC definition that make it suitable	
a.	How does Arry institutions?	
	for embedded applications?	
b.	With the help of an example, explain the hardware of an ARM based embedded	
	device.	
C.	What are banked registers? Explain.	
d.	Explain the three hardware extensions. ARM wraps around the core.	
		12
4.	Attempt any two of the followings	14
a.	With the help of an example, explain the addressing modes for load -store multiple	
	instructions.	
b.	Explain the two pseudoinstructions to move a 32-bit value into a register with	
	examples. How can we count leading zeroes?	
C.	What is thumb instruction set? Explain thumb register usage and ARM-thumb	
	· · · · · · · · · · · · · · · · · · ·	
d.	Explain the stack instructions and software interrupt instruction with examples.	
5.	Attempt and two of the following:	12
a.	Enumerate the rules of cycle timings for common instruction on the ARM91DMI.	
u.	What aparations are performed in parallel by the ARM9TDMI processor?	
b.	Why should register numbers be allocated variables? List the cases where physical	
U.	number of register is important.	
	Explain the different looping constructs of ARM.	
C.	Explain fixed-width bit field packing and unpacking and variable-width bitstream	
a C	Explain inxed-width on field packing and displaceing and various	

QP Code: 15717 [Total Marks: 60

## (2½ Hours)

N. B.:	(1)	All questions are compulsory.	
	(2)	Figures to the right indicate full marks.	
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	(4)	Symbols have their usual meanings and tables have their usual sta	andard design
		unless stated otherwise.	
	(5)	Use of calculators and statistical tables are allowed.	
Q.1	At	tempt <u>any two</u> of the following	(12)
a)		stify the following statement 'Brightness discrimination is poor at w levels of illumination'.	6
b)	W	rite and explain the expression for DFT applied to 2-D signal.	6
c)		hat is imaging in the ultraviolet band. Explain.	6 120
d) .	Define and explain the following: i) Types of connectivity ii) FFT		6 1
Q.2	At	tempt <u>any two</u> of the following	
- <b>X</b>	Autompt any two of the following		()(2)
a)		ate and explain the procedure to convert a non uniform histogram to	6
15		iform histogram.	*
b)		th an example explain the properties of first and second order rivative.	6
c)		scuss the low pass frequency domain filters in image enhancement.	
d)	Wr	ite the advantage of piecewise linear function. Hence explain any	6
۵	one	e method in this.	6
Q.3	Att	tempt any two of the following	(12)
a)	Sta	te and explain any two application of grey scale morphology.	6
b)	Ex	plain the procedure to construct the image pyramids.	6
c)	Но	w region filling algorithm works. Explain with an example.	6
d)		w HIT-or-MISS transformation is used for finding the local	6
7	pat	terns in image. Explain.	0
•			
Q.4	Att	empt <u>any two</u> of the following	(12)
a)	Ho	w does Huffman coding achieve compression. Explain.	6
b)	Des	scribe briefly LoG oberator.	6
c)	Dis	cuss the thinning algorithm for finding skeleton of region.	6
d)	Def	fine psychovisual redundancy. Explain an approach to	6
		ove/reduce (t)	
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a)	Wh	at are spatial and grey level resolution. Explain.	6
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	ima	ge.Explain.	•
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d) 八	<b>Exp</b>	lain the following: i) Global thresholding ii) Chain number.	6
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MG-Con. 6850-15.

**QP Code: 16068** 

(2½ hours)

Total Marks: 60

N. B.	: (1) All questions are compulsory.	
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	(3) Answers to the <u>same question</u> must be <u>written together</u> .	
	(4) Numbers to the <u>right</u> indicate <u>marks</u> .	
	(5) Draw <u>neat labeled diagrams</u> wherever <u>necessary</u> .	. `
	(6) Use of Non-programmable calculators is allowed.	N
	<i>₽</i>	1
1.	Attempt any two of the following:	12
a.		
	bit microcontrollers?	
b.	what are RISC and CISC processors? How can we distinguish between 8-bit and 109 bit microcontrollers?  Discuss the criteria for selecting a microcontroller device.	
C.	With the help of a block diagram explain the Amtel 89C51 microcontroller.	
d.	With the help of the circuit diagram, explain the test circuit for 89C2051	
٠.		
2.	Attempt <u>any two</u> of the following:	12
a.	Give an overview and features of peripheral interface controllers.	
b.	With the help of a block diagram the PIC 16C61.	
C.	Explain status registers and power control registers in PIC 16F8XX.	
d.	Explain capture, compare and PWM modules in PIC16F877.	
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3.	Attempt any two of the following:	12
	How does ARM instruction set differ from pure RISC definition that make it suitable	
a.	for embedded applications?	
L.	With the help of an example, explain the hardware of an ARM based embedded	, 10
b.	device.	
	What are banked registers? Explain.	
C.	Explain the three hardware extensions, ARM wraps around the core.	
d.	Explain the three hardware extensions, Taday was a	
	Attempt any two of the following:	12
4.	With the help of an example, explain the addressing modes for load –store multiple	
a.	With the nerp of an example, explain the addressing modes as a second	
	instructions.  Explain the two pseudoinstructions to move a 32-bit value into a register with	
b.	examples. How can we count leading zeroes?	
	What is thumb instruction set? Explain thumb register usage and ARM-thumb	
C.		
	interworking with examples.	
d.	Explain the stack instructions and software interrupt instruction with examples.	
	C an an	12
5.	Attempt any two of the following:	12
a.	Enumerate the rules of cycle timings for common instruction on the ARM9TDMI.	
	What operations are performed in parallel by the ARM9TDMI processor?	
Ъ.	Why should register numbers be allocated variables? List the cases where physical	
	number of register is important.	
C.	Explain the different looping constructs of ARM.	
d,	Explain fixed-width bit field packing and unpacking and variable-width bitstream	
5/1	packing.	

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